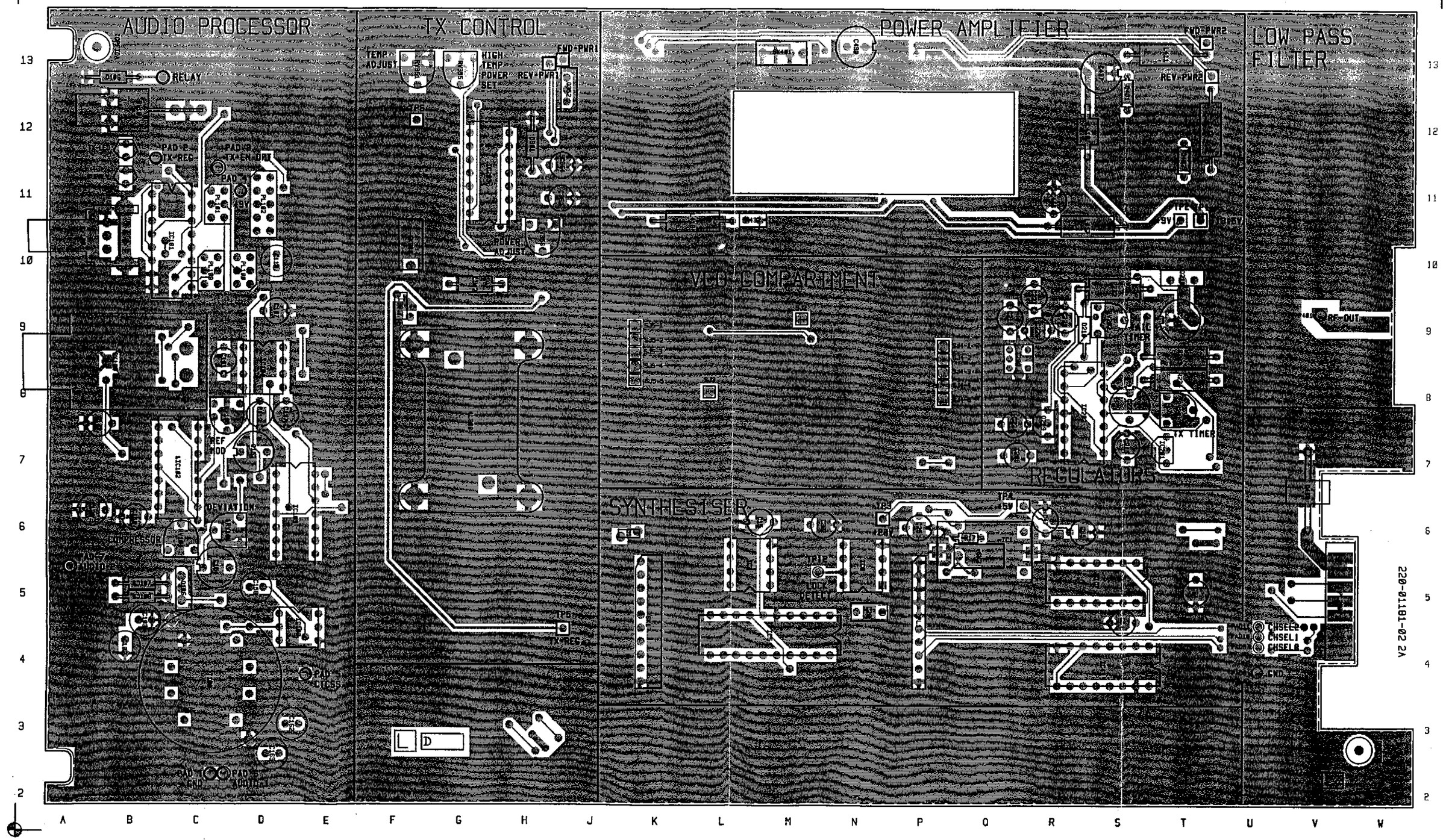


DIAGRAM 5 T881 PCB LAYOUT - TOP SIDE.

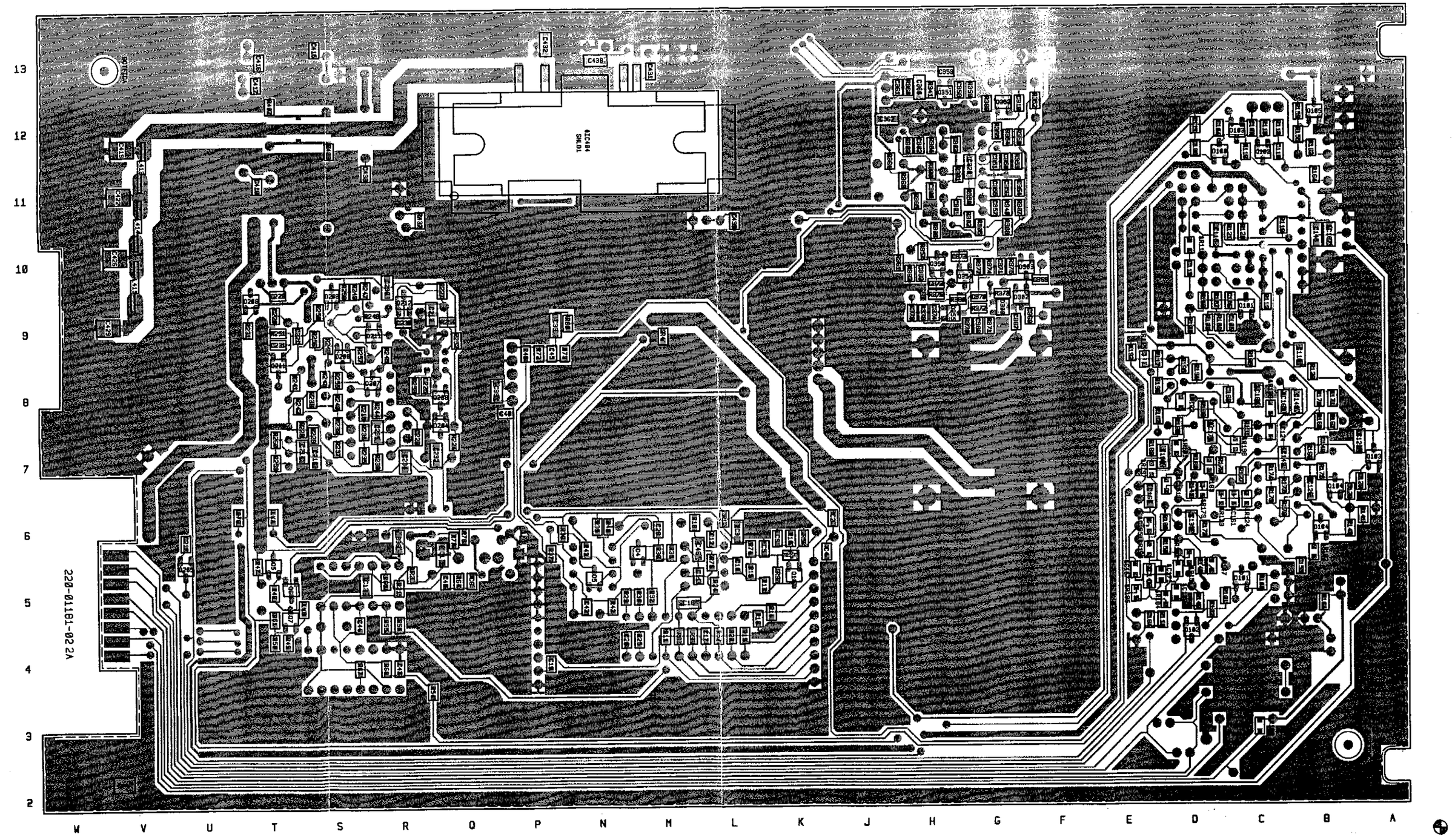
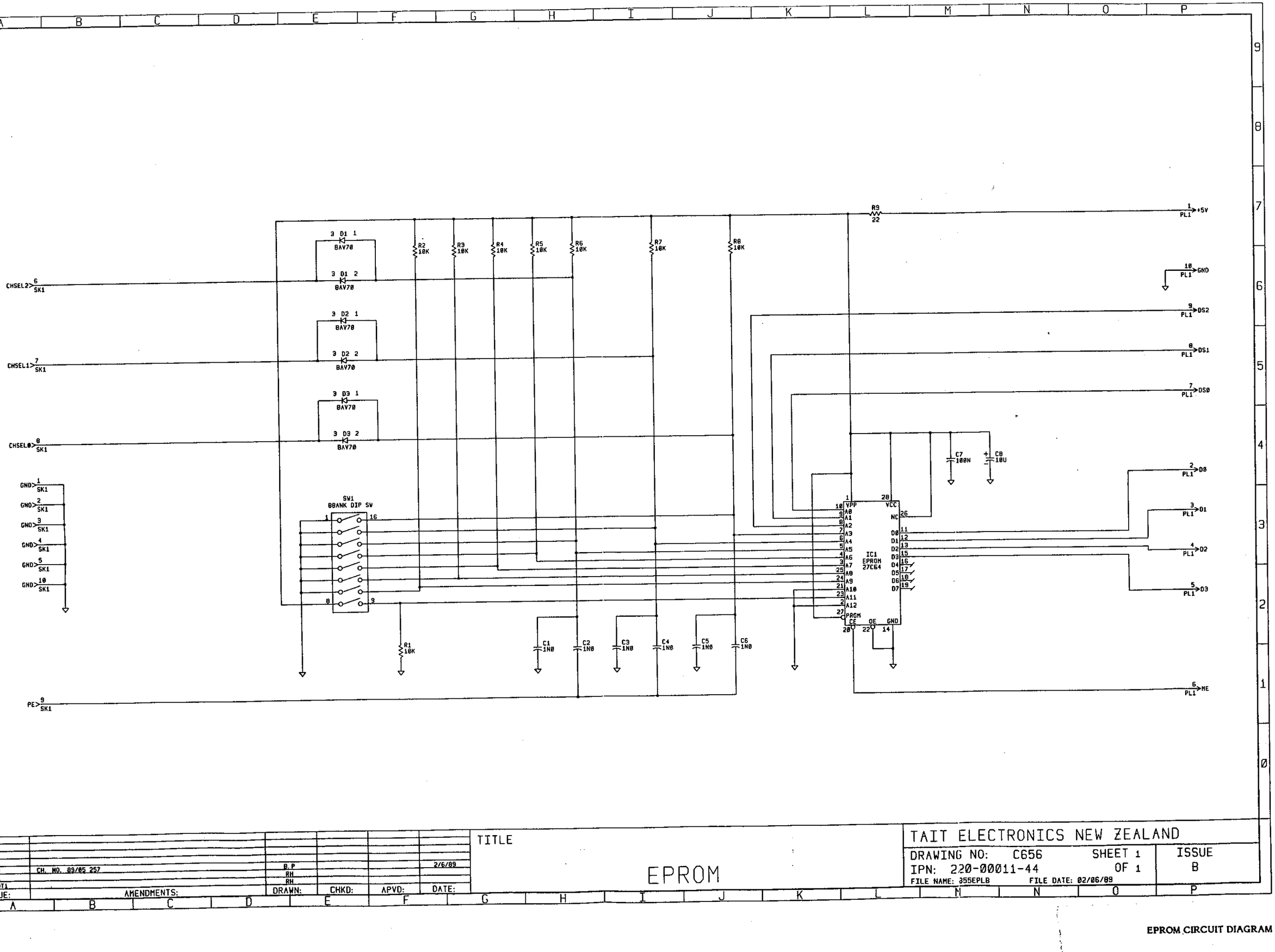


DIAGRAM 6 T881 PCB LAYOUT - BOTTOM SIDE.

T881 PCB LAYOUT - BOTTOM SIDE

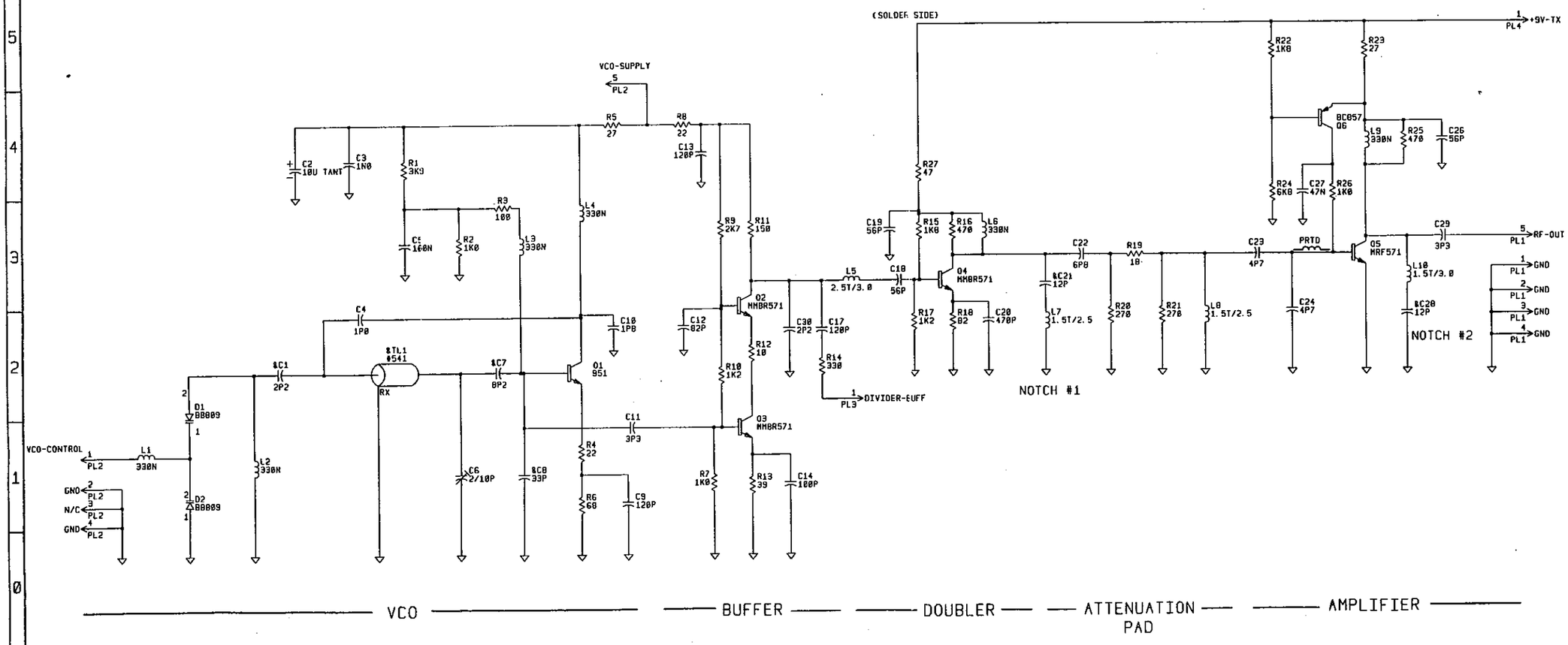


B	CH. NO. 89/05 257	B.P			2/6/89	TITLE	TAIT ELECTRONICS NEW ZEALAND		
A		RH				EPROM	DRAWING NO: C656	SHEET 1	ISSUE
PROTI		RH					IPN: 220-00011-44	OF 1	B
ISSUE:	AMENDMENTS:	DRAWN:	CHKD:	APVD:	DATE:		FILE NAME: 355EPLB	FILE DATE: 02/06/89	

EPROM CIRCUIT DIAGRAM

LAYOUT FOR RX LOW

&VARIANT		OUTPUT FREQ. MHZ	&TL1	&TL1 LENGTH	COMPONENTS				
					&C1	&C7	&C8	&C21	&C28
51	RX	755-835	851-00005-41	130MM	2P2S	8P2S	33PS	12PS	12PS
52	RX/TX	800-915	851-00005-61	125MM	1P8S	4P7S	27PS	10PS	10PS
54	TX	850-960	851-00005-42	120MM	1P8S	4P7S	27PS	10PS	8P2S



ISSUE:	AMENDMENTS:	DRAWN:	CHKD:	APVD:	DATE:
A	UPDATE FROM T99	WD			
T99	UPDATE FROM P2	WD			
PROTO2	UPDATE FROM PROTO1	BP			
PROT1	ORIGINAL	BP			

TITLE

T88X VCO

TAIT ELECTRONICS NEW ZEALAND

DRAWING NO: C712 SHEET 1 OF 1

IPN: 220-01184-01

FILE NAME: 88VCLA FILE DATE: 14/6/90

ISSUE A

VCO CIRCUIT DIAGRAM

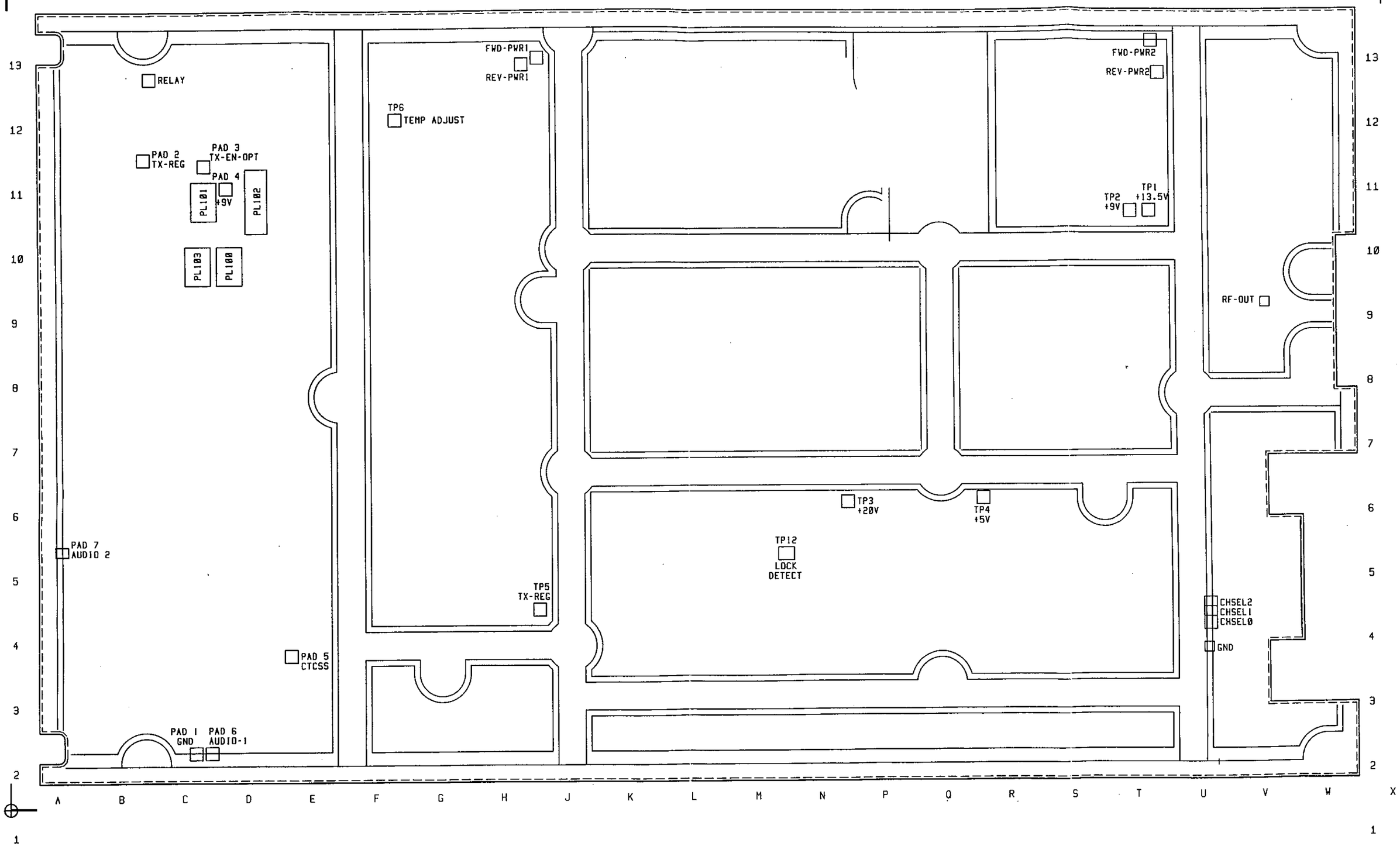
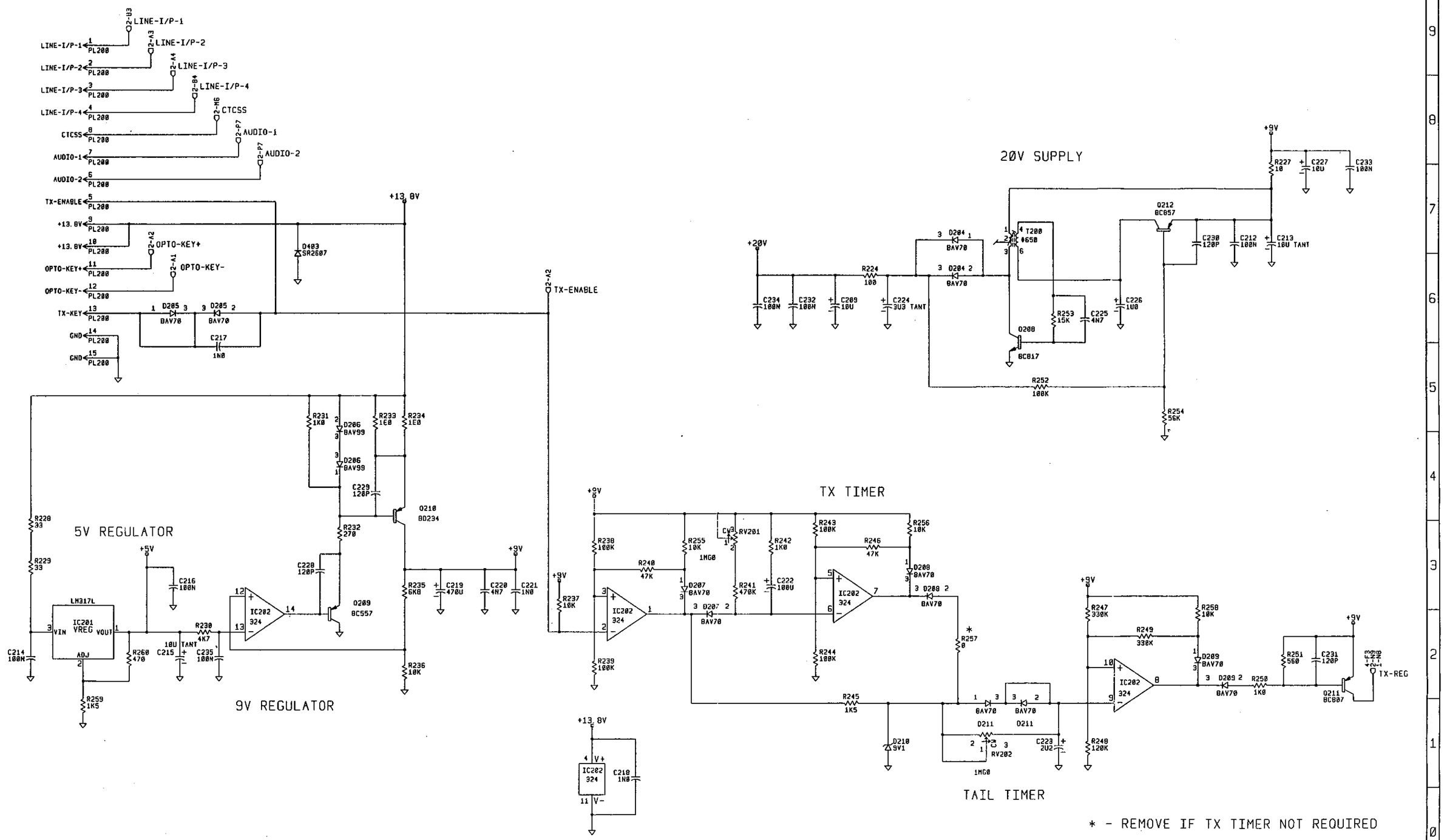


DIAGRAM 7: T881 TRANSMITTER TESTPOINT/OPTION DRAWING.

T881 TEST POINTS/OPTIONS - TOP SIDE



ISSUE:	AMENDMENTS:	DRAWN:	CHKD:	APVD:	DATE:
2A	91/08-743	M.CLYNE			
1C	ADDED ASSEMBLY DRAWINGS.	M.HALL	D.ELDER	D.ELDER	26/06/91
1B	C/N 91/03-157	MC			
1A	CH/N:90/10-520	WD			
0A	FIRST PRODUCTION RUN	BP	SP	NK	06/06/90
T99	T99 UPDATE	BP			
PROTO1	ORIGINAL	BP	NK	NK	

TITLE

T881 REGULATOR

TAIT ELECTRONICS NEW ZEALAND

DRAWING NO: C708 SHEET 3 OF 4 ISSUE 2A

IPN: 220-01181-02

FILE NAME: B81TXL2A FILE DATE: 30/10/91

REGULATOR CIRCUIT DIAGRAM

*TCXO	*IC1	*R17	*SK2
1.5PPM	ENC3003A	-	-
1.0PPM	TCXO-12.8M	-	-

TCXO 12.8MHZ

SIDE ENTRY COAX SKT

REFERENCE DIVIDER

800KHZ

PHASE MODULATOR

REF-MOD

200KHZ

VCO DIVIDER BUFFER

DIVIDER-BUFF

LOOP FILTER

VCO-MOD

VCO-CONTROL

VCO-O/P

TX-REG

LOCK-DETECT

TEST POINT LOCK-DETECT

LOCK DETECT

DIVIDER AND SYNTHESISER

FROM EPROM

TO EPROM

ISSUE:	AMENDMENTS:	DRAWN:	CHKD:	APVD:	DATE:
2A	91/08-743	M.CLYNE			
1C	ADDED ASSEMBLY DRAWINGS	M.HALL	D.ELDER	D.ELDER	26/06/91
1B	C/N 91/03-157	NC			
1A	CH/N: 90/10-528	WD			
PA	FIRST PRODUCTION RUN	BP	SP	NK	06/06/90
T99	T99 UPDATE	BP			
PROTO1	ORIGINAL	BP	NK	NK	

TITLE

T881 SYNTHESISER

TAIT ELECTRONICS NEW ZEALAND

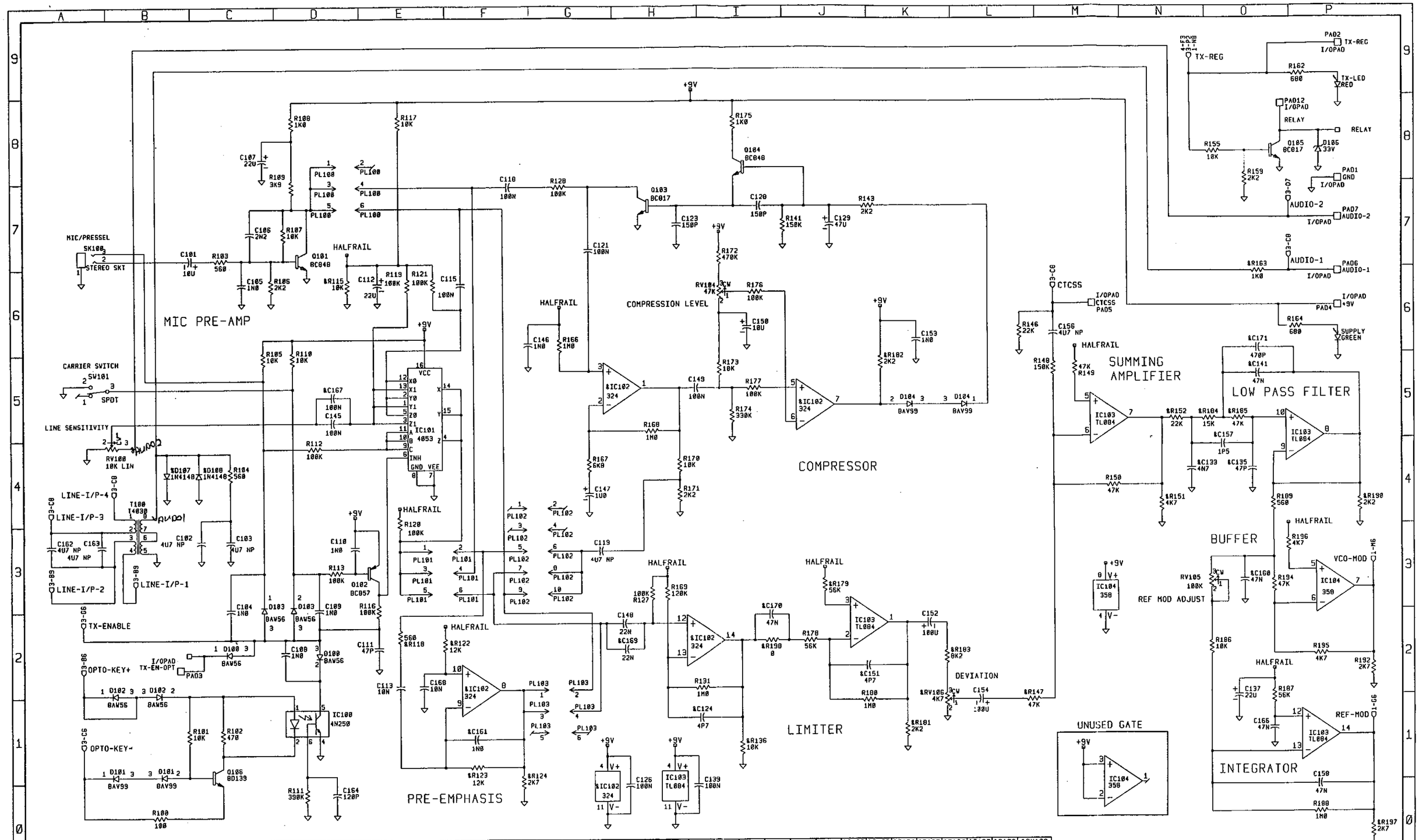
DRAWING NO: C708 SHEET 1 OF 4

IPN: 220-01181-02

ISSUE 2A

FILE NAME: 881TXL2A FILE DATE: 30/10/91

SYNTHESISER CIRCUIT DIAGRAM



&DEVIATION	&C124	&C133	&C135	&C141	&C151	&C157	&C160	&C161	&C167	&C169	&C170	&C171	&IC102	&R115	&R110	&R122	&R123	&R124	&R136	&R147	&R151	&R152	&R163	&R179	&R101	&R102	&R103	&R104	&R105	&R190	&R197	&R198	&RV106
2.5KHZ	4P7S	4N7S	47PS	47NT	4P7S	4P7S	47NT	1N0S	-	-	-	-	324	10KS	560S	12KS	12KS	2K7S	10KS	47KS	4K7S	27KS	1K0S	56KS	2K2S	2K2S	8K2S	10KS	56KS	2K2S	2K7S	0S	4K7VAR2
5KHZ	4P7S	4N7S	47PS	47NT	4P7S	1P5S	47NT	1N0S	-	-	-	-	324	10KS	560S	12KS	12KS	2K7S	10KS	47KS	4K7S	22KS	1K0S	56KS	2K2S	2K2S	8K2S	15KS	47KS	2K2S	2K7S	0S	4K7VAR2
10KHZ	-	-	220PS	-	-	-	-	220PS	100NT	22NS	47NT	470PS	TL084	12KS	0S	15KS	15KS	-	-	39KS	-	22KS	0S	1MG0S	-	-	2K2S	0S	22KS	-	-	-	10KVAR2

ISSUE:	AMENDMENTS:	DRAWN:	CHKD:	APVD:	DATE:
2A	91/80-743	M. CLYNE			
1C	ADDED ASSEMBLY DRAWINGS.	M. HALL	D. ELDER	D. ELDER	26/06/91
1B	C/N 91/03-157	MC			
1A	CH/N: 90/10-528	WD			
0A	FIRST PRODUCTION RUN	BP	SP	NK	06/06/90
T99	T99 UPDATE	BP			
PROTO1	ORIGINAL	BP	NK	NK	

TITLE

T881
AUDIO PROCESSOR

TAIT ELECTRONICS NEW ZEALAND

DRAWING NO: C708 SHEET 2 OF 4

IPN: 220-01181-02

FILE NAME: 881TXL2A FILE DATE: 30/10/91

ISSUE 2A

AUDIO PROCESSOR CIRCUIT DIAGRAM

#FREQUENCY	#IC404	#L300
800-880MHZ	MHW806A2	HEL564
850-960MHZ	MHW806A4	HEL565

POWER DETECT

TEMPERATURE CONTROL

POWER CONTROL

BAND PASS PRE FILTER

RF SWITCH

POWER AMPLIFIER

DIRECTIONAL COUPLER

LOW PASS FILTER

ISSUE:	AMENDMENTS:	DRAWN:	CHK'D:	APV'D:	DATE:
2A	91/09-743	M. CLYNE			
1C	ADDED ASSEMBLY DRAWINGS.	M. HALL	D. ELDER	D. ELDER	26/06/91
1B	C/N 91/03-157	MC			
1A	CH/N 90/10-528	WD			
0A	FIRST PRODUCTION RUN	BP	SP	NK	05/05/90
T99	T99 UPDATE	BP			
PROTO1	ORIGINAL	BP	NK	NK	30/10/91

TITLE

T881
EXCITER / P. A.

TAIT ELECTRONICS NEW ZEALAND

DRAWING NO: C708 SHEET 4 OF 4

IPN: 220-01181-02

FILE NAME: 881TXL2A FILE DATE: 30/10/91

ISSUE 2A

EXCITER/PA CIRCUIT DIAGRAM